



# Open-source hardware in the European Chips Act

Go-IT Webinar

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# EC Open Source Hardware Roadmap

# Recommendations and Roadmap for European Sovereignty in Open-Source Hardware, Software, and RISC-V Technologies



## Recommendations and Roadmap for European Sovereignty in Open Source Hardware, Software, and RISC-V Technologies

Report from the  
Open Source Hardware & Software Working Group

August 2022



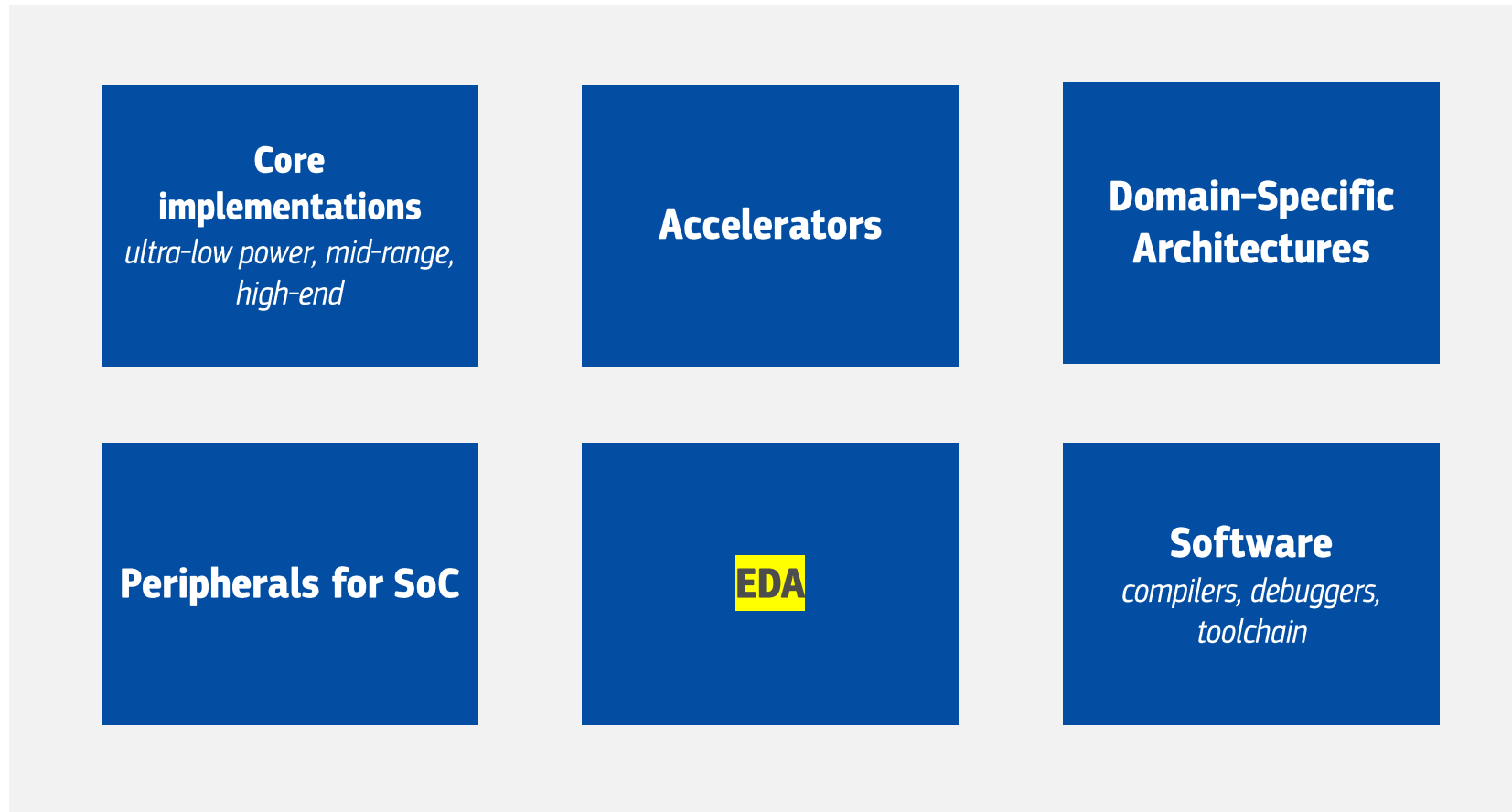
## Key outcomes:

- Build a critical mass of European open-source hardware/software
- Develop both open-source hardware and software as they are interdependent
- Address cross-cutting issues
- Cultivate innovation
- Engage with the open-source community

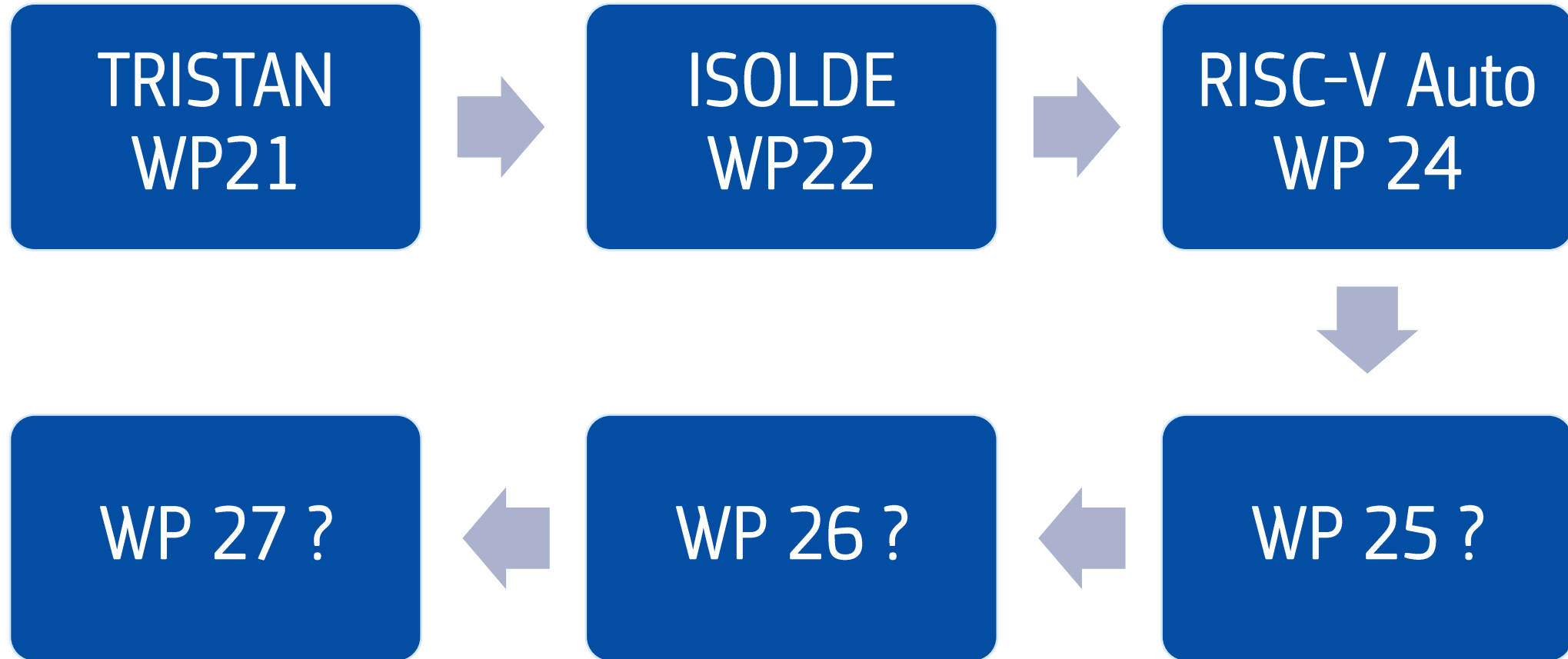
# Recommendations and Roadmap for European Sovereignty in Open-Source Hardware, Software, and RISC-V Technologies

- Open-source hardware is **a key sovereignty tool in line with Chips Act priorities**, providing Europe with an alternative to licensing IPs from non-EU third parties.
- A key success criterion for this is for Europe to develop **a fully blown open-source ecosystem**.
- This report has defined a strategic roadmap considering **short (2-5 years), medium (5-10 years) and long term (>10 years)** goals.
- The success of the roadmap depends on European actors working closely together to **create a critical mass of activities** that enhance and expand the European open-source community and its influence.

# Recommendations and Roadmap for European Sovereignty in Open-Source Hardware, Software, and RISC-V Technologies



## Next steps – Chips JU



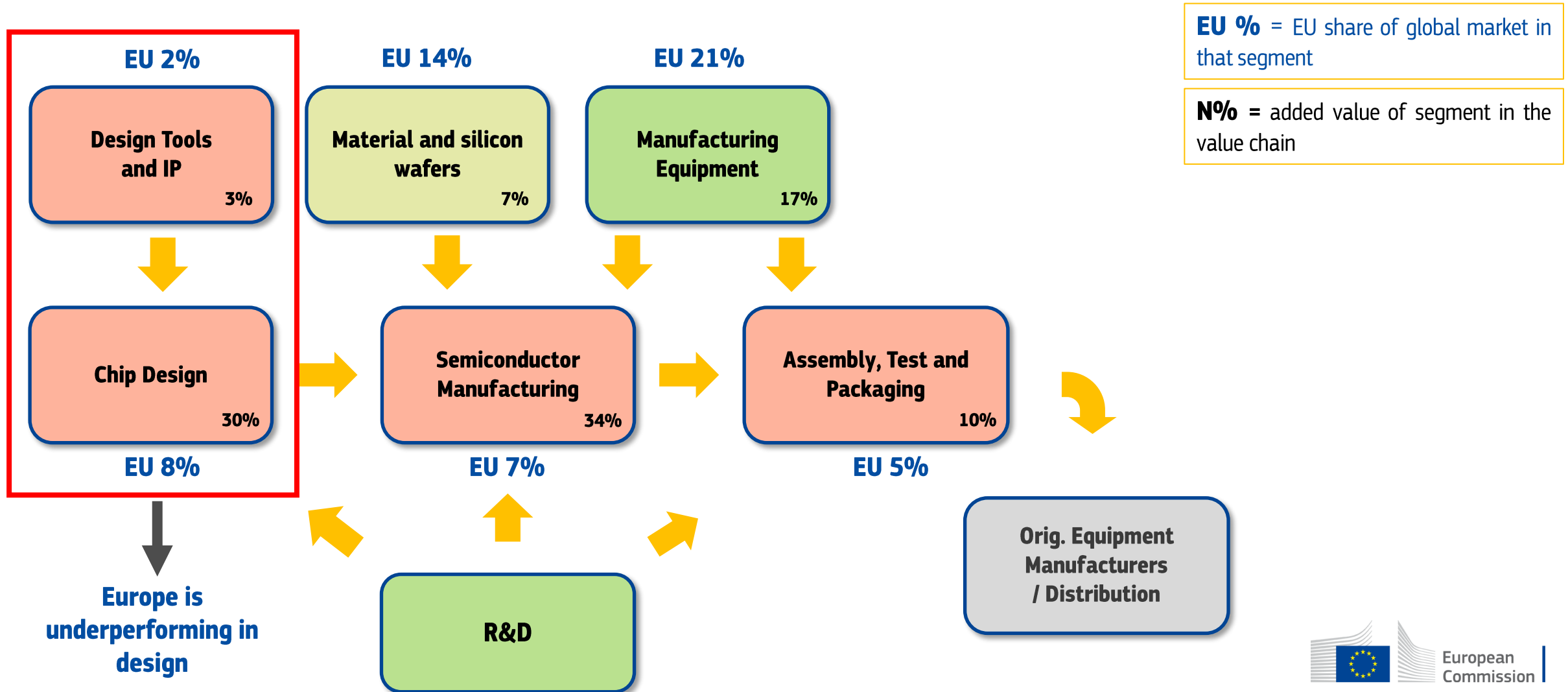
# The European Chips Act

# The EU Chips Act - rationale for intervention

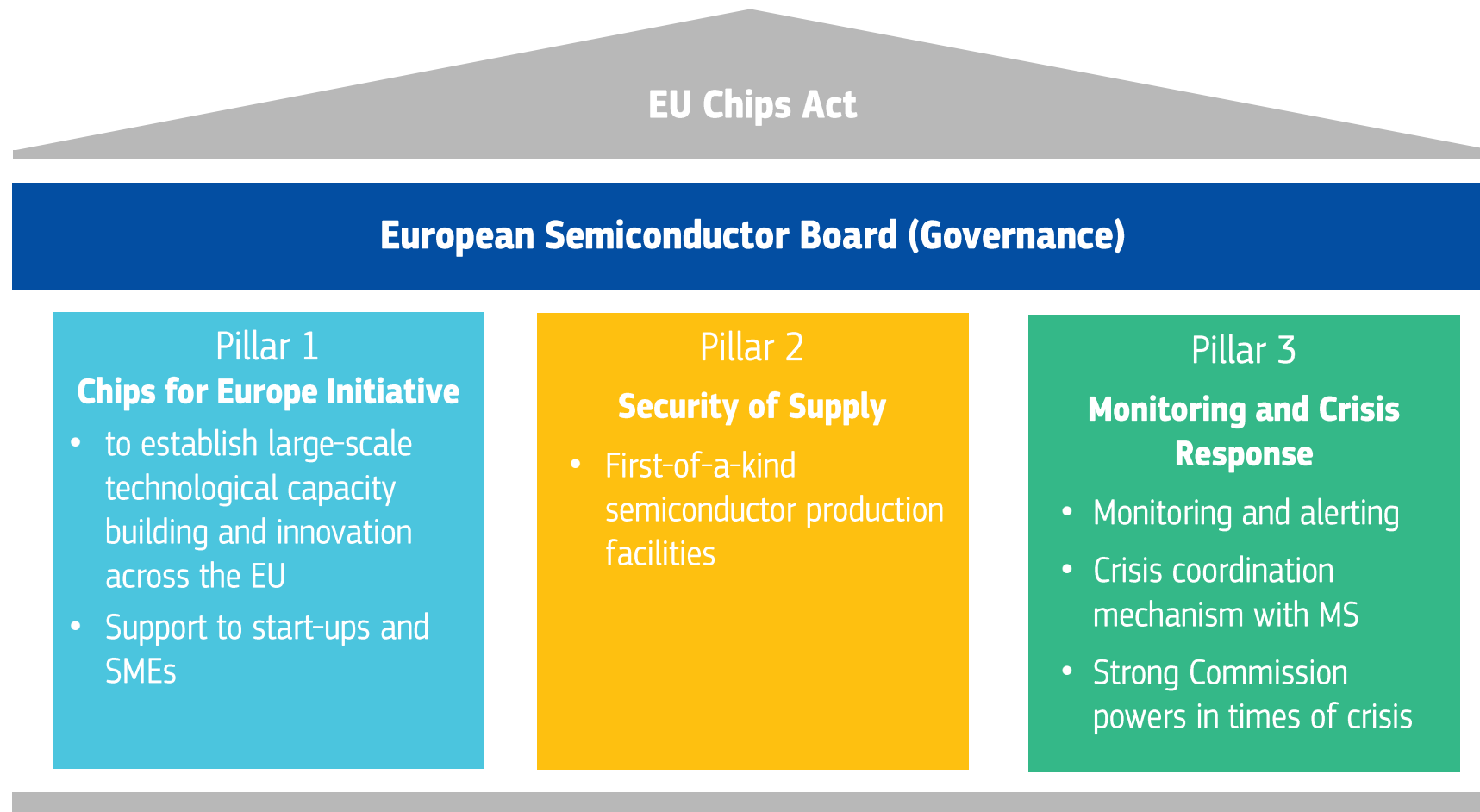




# The semiconductors value-chain in the EU



# Three pillars of the Chips Act



# Chips for Europe Initiative

Aim: bridging the gap from lab to fab

5 Objectives

- 1 Reinforce design capacity by providing a **virtual design platform**
- 2 Enhance existing and developing new **pilot lines**
- 3 Accelerate the development of **quantum chips**
- 4 Expand **skills** and set up a network of **competence centres**
- 5 Facilitate SME access to **equity and loans** through a dedicated **Chips Fund**

Chips JU

EIC  
I-EU

Basic  
Research

Applied  
Research

Prototyping

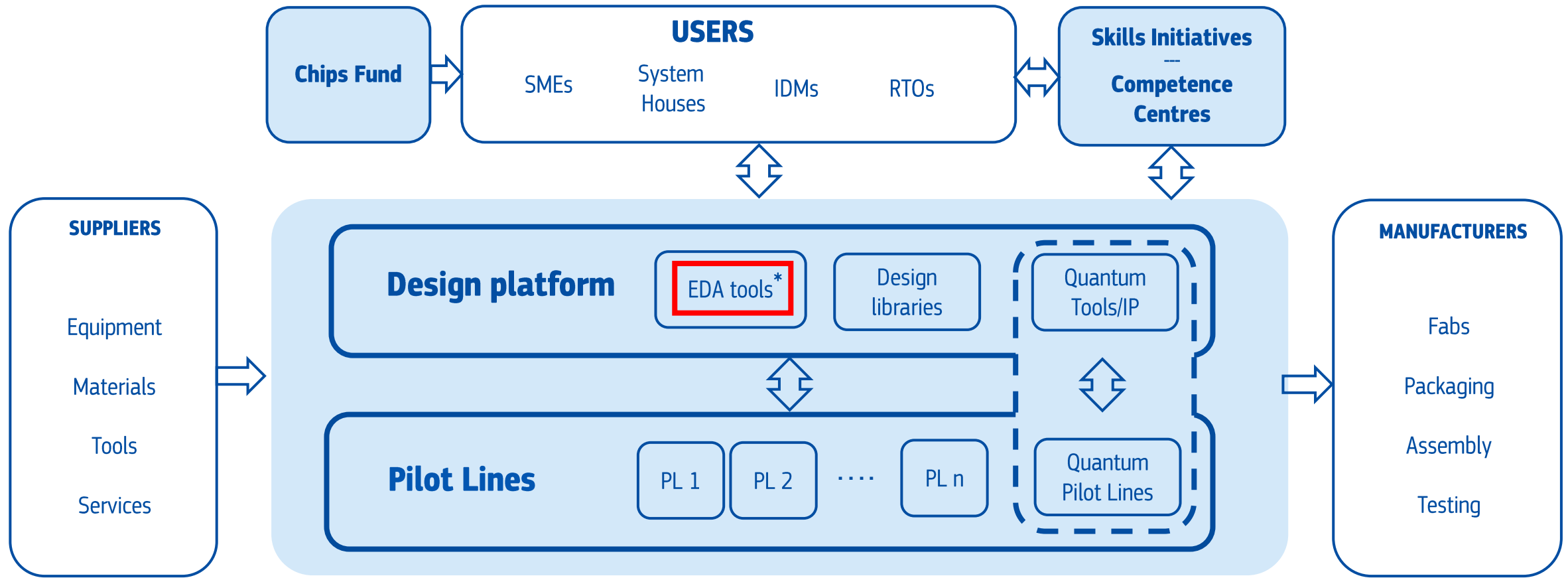
Pilot lines

Production

# Chips for Europe Initiative

## Bridging the gap from lab to fab

**EUR 6 billion+ EU & Member States funding foreseen**



# Thank you

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